WEST Search History

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DATE: Thursday, November 10, 2005

Hide?	Set Name	Query	Hit Count
		DB=PGPB; PLUR=YES; OP=ADJ	
☐	L5	(memory device and interface and write enable connection and column address strobe connection and row address strobe connection and chip select connection).clm.	1
☐	L4	(memory device and interface and write enable connection and column address strobe connection and row address strobe connection and chip select connection).clms.	0
☐	L3	(synchronous non-volatile memory device and interface and write enable connection and column address strobe connection and row address strobe connection and chip select connection).clms.	0
☐	L2	(synchronous non-volatile memory device and interface and write enable connection and column address strobe connection and row address strobe connection and chip select connection).clm.	0
		DB=USPT; PLUR=YES; OP=ADJ	
☐	L1	(5737258 or 5986918).pn.	2

END OF SEARCH HISTORY